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Bib Data Sheet

CONFIRMATION NO. 4255

DID Data Chicat									
SERIAL NUMBER 10/019,437		FILING OR 371(c) DATE 12/31/2001 RULE	C	CLASS 345		GROUP ART UNIT 2675		ATTORNEY DOCKET NO. 60188-124	
APPLICANTS				***					
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Foreign Priority claimed					SHEETS TOT DRAWING CLAI 11 14		MS	INDEPENDENT CLAIMS 4	
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TITLE	ing ci	rcuit, semiconductor in	tegrated	circuit device,	refere	nce volt	age buff	ering (circuit, and
						All Fees			
						☐ 1.16 Fees (Filing)			
	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT No for following:					☐ 1.17 Fees (Processing Ext. of time)			
1272						1.18 Fees (Issue)			
							Other		
							Credit		
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